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1 [Total power optimization through simultaneously multiple- \$v_{DD}\$ multiple- \$v_{TH}\$ assignment and device sizing with stack forcing](#)
[W. Hung, Y. Xie, N. Vijaykrishnan, M. Kandemir, M. J. Irwin, Y. Tsai](#)

 August 2004 **ISLPED '04**: Proceedings of the 2004 international symposium on Low power electronics and design

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In this paper, we present an algorithm for the minimization of total power consumption via multiple VDD assignment, multiple VTH assignment, device sizing and stack forcing, while maintaining performance requirements. These four ...

Keywords: genetic algorithm, low power

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